



# CHAIBASA ENGINEERING COLLEGE

(Estd. By Govt. of Jharkhand & Run By Techno India under PPP)



## WEBINAR SERIES

*Knowledge  
Booster*

*Gateway  
Challenge*

Theme  
of  
**Interactive Webinar**



**C-MOS Logic Design  
for Electronics  
Engineers**

**20.05.2020 to 21.05.2020  
6.00 PM- 7.00 PM**

**Speaker: Dr. Indrajit Chakrabarti  
Professor  
IIT, Kharagpur**

**TEQIP-3**

Conducted by: **Electronics & Communication  
Engineering Department**  
Sponsored by: **TEQIP-III**